Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1.	(Currently Amended) A dielectric thin film including a bismuth layer
compound ha	aving a c-axis oriented vertically with respect to a substrate surface, wherein:
	the bismuth layer compound is composed of a thin film capacitance element
composition,	wherein: composition;
	athe bismuth layer compound having a c axis oriented vertically with respect
to a substrate	surface is expressed by a composition formula of (Bi ₂ O ₂) ²⁺ (A _{m-1} B _m O _{3m+1}) ²⁻
or Bi ₂ A _{m-1} B _r	n O _{3m+3} , wherein wherein:
	"m" is an even number,
	"A" is at least one element selected from Na, K, Pb, Ba, Sr, Ca and Bi,
and	
	"B" is at least one element selected from Fe, Co, Cr, Ga, Ti, Nb, Ta,
Sb, V, Mo ar	nd W; and
	Bi in said bismuth layer compound is excessively included with respect to said
composition	formula of $(Bi_2O_2)^{2+}$ $(A_{m-1} B_m O_{3m+1})^{2-}$ or $Bi_2A_{m-1} B_m O_{3m+3}$, and the excessive
content of Bi	is in a range of $0 < Bi < 0.5xm$ mol in terms of Bi.
2.	(Currently Amended) The dielectric thin film capacitance element
composition	as set forth in claim 1, wherein the excessive content of Bi is in a range of $0.4 \le$
Bi < 0.5xm n	nol in terms of Bi.
3.	(Currently Amended) A dielectric thin film including a bismuth layer
compound ha	aving a c-axis oriented vertically with respect to a substrate surface, wherein:
	the bismuth layer compound is composed of a thin film capacitance element
composition	wherein a composition:

the bismuth layer compound having a c-axis oriented vertically with respect to a substrate surface is expressed by a composition formula of SrBi₄Ti₄O₁₅; and Bi in-said the bismuth layer compound is excessively included with respect to said composition formula of SrBi₄Ti₄O₁₅, and the excessive content of Bi is in a range of 0 < Bi $< \frac{2.0}{2.0}$ 0.5xm mol in terms of Bi. 4. (Currently Amended) A dielectric thin film including a bismuth layer compound having a c-axis oriented vertically with respect to a substrate surface, wherein: the bismuth layer compound is composed of a thin film capacitance element composition, wherein a composition; the bismuth layer compound having a c-axis oriented vertically with respect to a substrate surface is expressed by a composition formula of SrBi₄Ti₄O₁₅; and Bi in-said the bismuth layer compound is excessively included with respect to said composition formula of SrBi₄Ti₄O₁₅, and when the excessive content of Bi is expressed by a mole ratio (Bi/Ti) against Ti, Bi/Ti is in a range of 1 < Bi/Ti < 1.5. 5. (Currently Amended) A dielectric thin film including a bismuth layer compound having a c-axis oriented vertically with respect to a substrate surface, wherein: the bismuth layer compound is composed of a thin film capacitance element composition, wherein composition; athe bismuth layer compound having a c-axis oriented vertically with respect to a substrate surface is expressed by a composition formula of Sr_xCa_yBa_zBi₄Ti₄O₁₆, x+y+z=1, $0 \le x \le 1$, $0 \le y \le$ and $0 \le z \le 1$; and Bi in-said the bismuth layer compound is excessively included with respect to said composition formula of Sr_xCa_yBa_zBi₄Ti₄O₁₅, and when the excessive content of Bi is expressed by a mole ratio (Bi/Ti) against Ti, Bi/Ti is in a range of 1 < Bi/Ti < 1.5.

- 6. (Currently Amended) The <u>dielectric</u> thin film <u>capacitance element</u>
 eomposition as set forth in claim 1, the thin film <u>capacitance element composition</u>
 furthermore including a <u>further comprising at least one</u> rare earth element (at least one selected from the group consiting of Sc, Y, La, Ce, Pr, Nd, Pm, Sm, Eu, Gd, Tb, Dy, Ho, Er, Tm, Yb and <u>Lu</u>) <u>Lu</u>.
- 7. (Currently Amended) The <u>dielectric</u> thin film-eapacitance element eomposition as set forth in claim 1, wherein a c-axis orientation degree of said the bismuth layer compound with respect to-said the substrate surface is 80% or more.
- 8. (Currently Amended) The <u>dielectric</u> thin film-eapacitance element eomposition as set forth in claim 1, wherein leakage current density at electric field intensity of 50 kV/cm is 1x10⁻⁷ A/cm² or lower.
- 9. (Currently Amended) The dielectric thin film-capacitance element eomposition as set forth in claim 1, wherein an average change rate of a capacitance against a temperature in a range of -55 to +150°C is ±500 ppm/°C with the reference temperature of 25°C.
- 10. (Currently Amended) A thin film capacitance element, wherein a lower portion electrode, the dielectric thin film as set forth in claim 1, and an upper portion electrode are successively formed on a substrate, wherein

said dielectric film is composed of the thin-film capacitance element composition as set forth in claim 1.

- 11. (Original) The thin film capacitance element as set forth in claim 10, wherein a thickness of said dielectric thin film is 1 to 1000 nm.
- 12. (Currently Amended) A thin film multilayer capacitor, wherein a plurality of the dielectric thin films as set forth in claim 1 and internal electrode thin films are alternately stacked on a substrate, wherein

said dielectric thin film is composed of the thin-film capacitance element composition as set forth in claim 1.

13. (Original) The thin film multilayer capacitor as set forth in claim 12, wherein a thickness of said dielectric thin film is 1 to 1000 nm.

14-24. (Canceled)